



OnLine Lecture 137: Introduction to Discontinuities

- Why this topic is important
 - ✓ Changes in the impedance a signal sees due to physical design feature is absolutely unavoidable
 - ✓ All we can hope to do is optimize the physical design to minimize their impact
 - ✓ By understanding how discontinuities created by geometry changes affect signals, we can identify what is important and equally of value, what is not, in optimizing the physical design of a system
- Level
 - ✓ intermediate
- Recommended prerequisites:
 - ✓ OLL-105 Impedance in the time and frequency domain
 - ✓ OLL-115 characteristic impedance
 - ✓ OLL-120 modeling transmission lines
 - ✓ OLL-135 reflections in the time domain

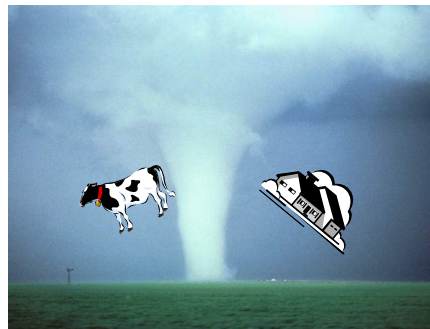


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Outline

- Four important classes of discontinuities
- The reflection and transmission properties of each type of discontinuity
- How much noise is too much
- The role of signal rise time
- General behavior, simulations, rules of thumb and design guidelines



Golden rule to minimize reflection noise:

Keep the instantaneous impedance the signal sees constant throughout its path.

The Strategy For Designing Out Discontinuities



1. Follow guidelines to improve our aim when we shoot from the hip

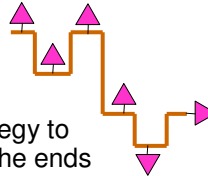
2. System level numerical simulations!

Best Design Practices (OLL-136 Terminations and Topologies)

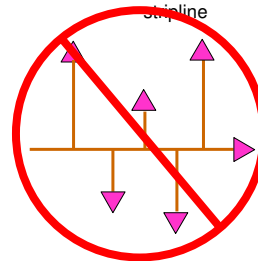
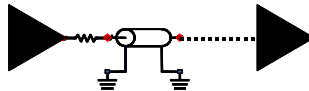
- Use controlled impedance interconnect



- Route with a linear topology



- Implement a termination strategy to manage the reflections from the ends

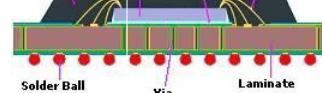
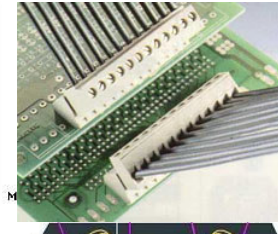
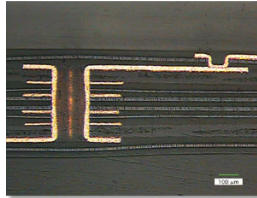


- **Minimize discontinuities along the line**

What is a Discontinuity?

A discontinuity: a change in the instantaneous impedance the signal sees

- Vias- layer transitions
- Connectors
- Package leads
- Solder balls
- Stubs
- 90 degree bends, or corners
- Test pads
- Neck downs when passing through a via field
- Engineering change wires
- Gaps in the return path- intentional and unintentional



Why Worry

- Problems from discontinuities:
 - ✓ Reflection noise
 - ✓ Rise time degradation
 - ✓ Possible enhanced cross talk: switching noise and ground bounce (OLL-220 Switching noise and ground bounce)
- Solutions
 - ✓ Minimize geometry changes
 - ✓ Compensation
 - ✓ Simulate to evaluate if it is a problem
 - ✓ OLL-138 Via design
 - ✓ OLL-139 Optimizing the physical design of discontinuities.

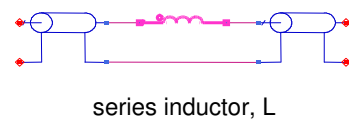
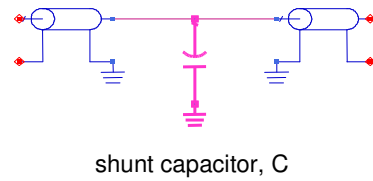
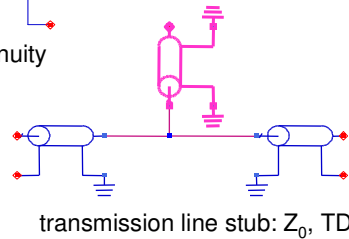
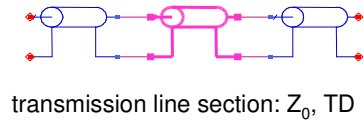
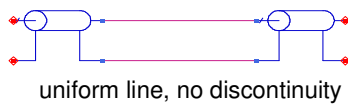


What to Watch For

- Reflected noise back to the driver
- Transmitted noise into the receiver
- Delay adder
- Rise time degradation

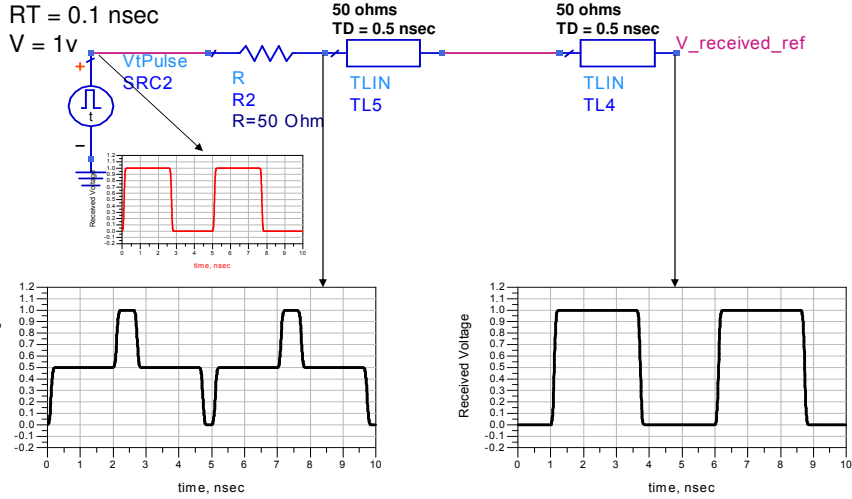


Four Fundamental Models for Discontinuities





Series Terminated Uniform Line: Baseline

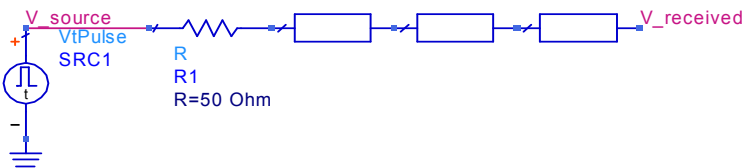


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Series Uniform Transmission Line Segment



- Parameters:
 - ✓ Z_0
 - ✓ TD
- Examples:
 - ✓ Neck downs through via fields
 - ✓ Surface trace passing under component
 - ✓ Package trace
 - ✓ Poorly designed buried layer

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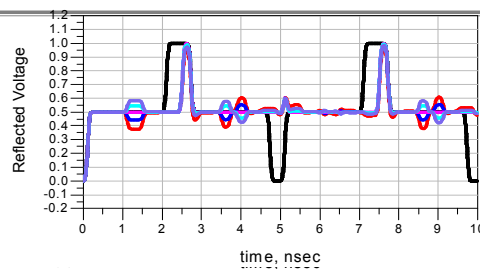


What to Expect

- Reflected signal:
 - ✓ Positive if $Z_0 > 50$ ohms, negative if $Z_0 < 50$ ohms
 - ✓ If $2 \times TD > RT$, should see flat bottom
 - ✓ If $2 \times TD < RT$, will see a pulse
 - ✓ $\rho \sim (Z_0 - 50 \text{ Ohms})/100 \text{ Ohms}$
 - ✓ For $< 10\%$ reflection noise, keep Z_0 within 10 ohms of 50 ohms: 40-60 ohms
 - ✓ As TD goes down, can allow larger impedance change: $\rho \sim 2 \times TD/RT \times \rho_{long}$
- Transmitted signal:
 - ✓ Same conditions should apply



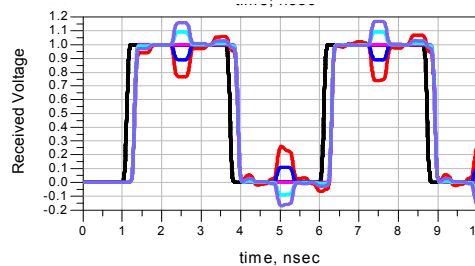
Effect of the T Line Impedance



Black trace is with no discontinuity

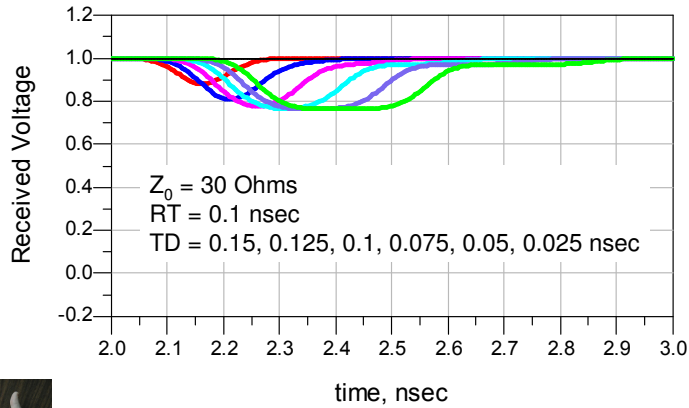
TD = 0.2 nsec
(RT = 0.1 nsec)

$Z_0 = 30, 40, 50, 60, 70$ Ohms





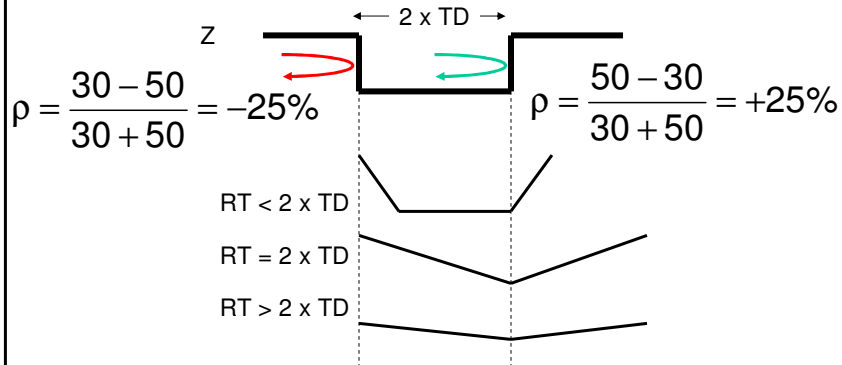
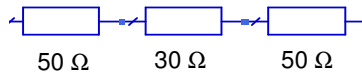
Effect of the T Line Time Delay



Keep TD < 50% RT



Time Delay and Rise Time Interactions

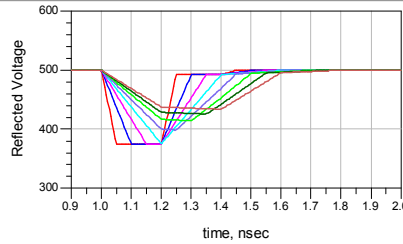




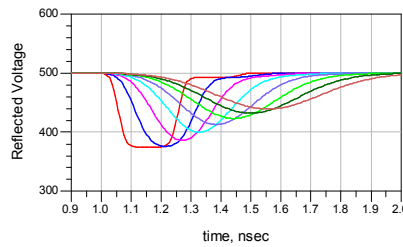
Effect of the Signal Rise Time

TD = 0.1 nsec
RT =

- 0.5 x TD
- 1 x TD
- 1.5 x TD
- 2 x TD
- 2.5 x TD
- 3 x TD
- 3.5 x TD
- 4 x TD



Reflected amplitude is reduced when RT > 2 x TD (teal trace)



When the edge is not an ideal ramp (Gaussian), estimate is less precise (rise time is not 0-100)



Summary of Guidelines for transmission line segment

- Reflected noise:

✓ If $2 \times TD > RT$

$$Noise\% = \frac{V_{reflection}}{V_{signal}} \sim \frac{\Delta Z}{100}$$

✓ If $2 \times TD < RT$

$$Noise\% = \frac{V_{reflection}}{V_{signal}} \sim \frac{\Delta Z}{100} \times \frac{2 \times TD}{RT}$$

- Design guidelines (for < 10% noise)

✓ Keep impedance within +/- 10 ohms of 50: $40 < Z_0 < 60$

✓ If Z_0 is more than 10 Ohms off, keep $TD < 0.5 \times RT$

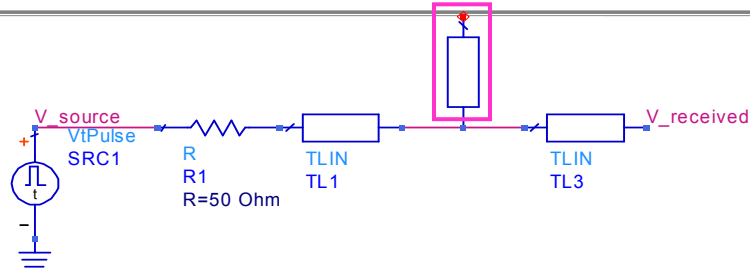
✓ If Z_0 is more than 20 Ohms off, keep $TD < 0.2 \times RT$ (20% RT)

✓ **Len (in inches) < RT (nsec)**





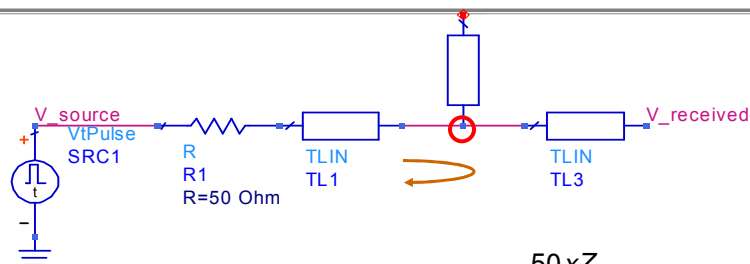
Transmission Line Stub



- Parameters:
 - ✓ Z_0
 - ✓ TD
- Examples:
 - ✓ Routing stubs
 - ✓ Plating stubs in packages
 - ✓ Through hole via stubs



What to Expect

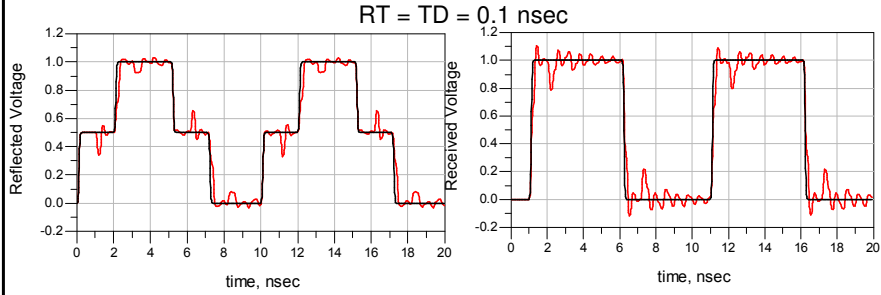


- Reflected signal will have a negative dip.
 - ✓ Lower impedance of stub, larger the dip
 - ✓ Longer TD, larger the dip
- Received signal will see the dip from second incidence reflection
- Lower impact from higher stub impedance, shorter time delay

$$\rho = \frac{50 \times Z_0 - 50}{50 + Z_0} \sim \frac{-1}{1 + \frac{Z_0}{25}}$$



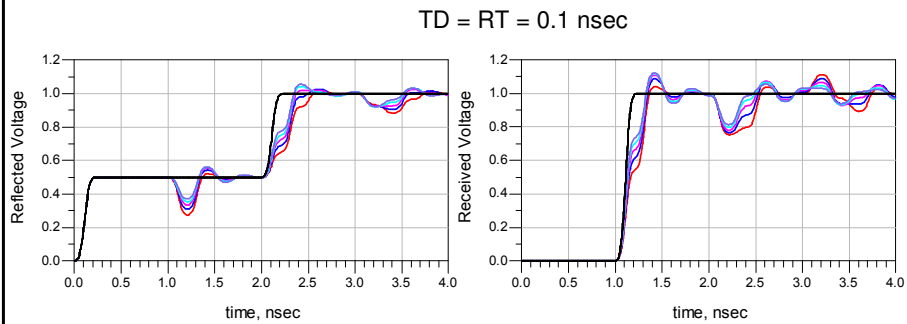
General Features of Stub Effects: $Z_0 = 50 \text{ Ohms}, TD = RT$



- Received signal:
 - ✓ Ringing last for a a multiple round trip times (3 x 1 nsec)
 - ✓ Undershoot at 1 nsec from reflection from the stub can cause false switching
- Reflected signal:
 - ✓ Large dip from the stub

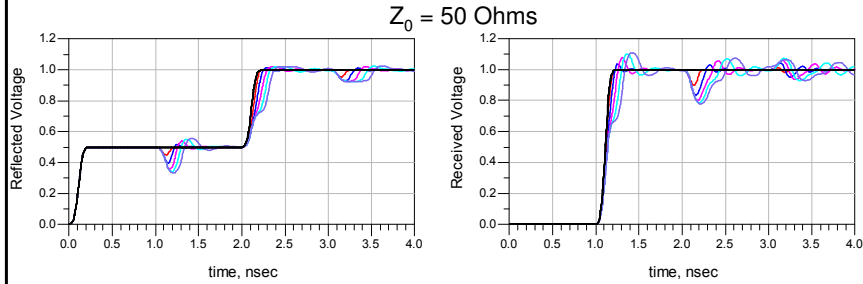


Effect of the Impedance of the Stub: $Z_0 = 30\Omega, 40\Omega, 50\Omega, 60\Omega, 70\Omega,$



- Noise magnitude roughly High impedance for the stub is lower noise in reflection
- Decreasing impedance, not a big impact on the received noise

Effect of the Time Delay of the Stub: TD = RT, 80%, 60%, 40%, 20%



- Shorter TD reduces received noise
 - ✓ Provided TD < 50% RT
 - ✓ Less than 10% noise if TD < 20% RT

Guidelines for Stub Design

- Higher impedance stubs will reflect less noise
- Reflected noise back to the driver is $\rho \sim \frac{-1}{1 + \frac{Z_0}{25}}$
- Reflection noise at the receiver is ~ 20%
- If TD < 0.5 x RT, received signal will decrease



Very Useful Rule of Thumb

- Keep TD < 20% RT
- Len of discontinuity = TD x v = TD x 6 inch/nsec
- Len < 20% x v x RT = 1.2 x RT ~ RT



Len (in inches) < RT (nsec)

Examples:

RT = 1 nsec, Len < 1 inch

RT = 0.25 nsec, Len < 0.25 inches

RT = 0.1 nsec, Len < 0.1 inches

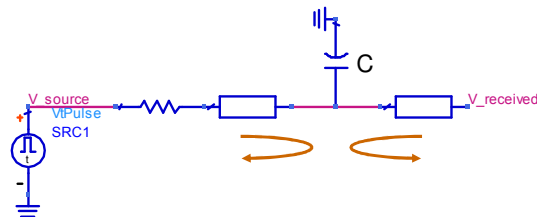


***This concludes session A.
Please return to
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start session B***

Welcome back to session B of OnLine Lecture OLL137 Introduction to Discontinuities

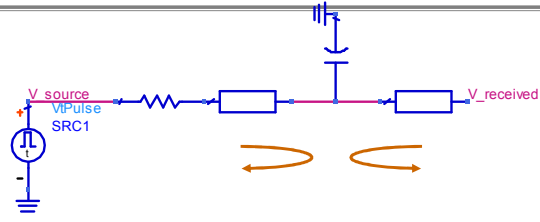
Shunt C Discontinuities

- Examples
 - ✓ Via capture pads
 - ✓ Test points
 - ✓ Component leads
 - ✓ Empty connectors
 - ✓ 90 degree bends

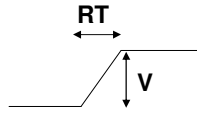




Impact from Shunt C Discontinuities



$$Z_{cap} = \frac{V}{C \frac{dV}{dt}} = \frac{V}{C \frac{V}{RT}} = \frac{RT}{C}$$



Shunt impedance

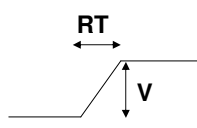
Examples:

RT = 1 nsec, C = 1 pF, $Z_{cap} \sim 1000$ Ohms

RT = 0.1 nsec, C = 1 pF, $Z_{cap} \sim 100$ Ohms



Estimating Reflected Noise



$$Z_{cap} = \frac{V}{C \frac{dV}{dt}} = \frac{V}{C \frac{V}{RT}} = \frac{RT}{C}$$

$$\rho \sim \frac{-1}{1 + \frac{Z_{Cap}}{25}}$$

$$\rho \sim \frac{-1}{1 + \frac{RT}{25 \times C}} \sim -\frac{25 \times C}{RT}$$

Examples:

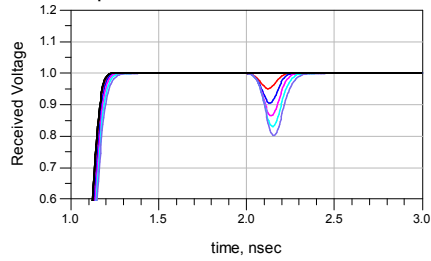
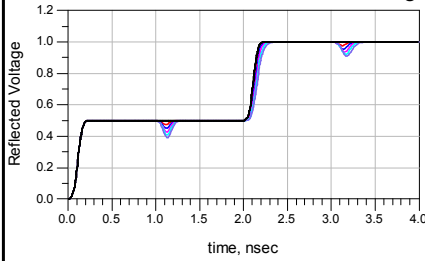
RT = 1 nsec, C = 1 pF, rho $\sim -0.025/1 \sim -2.5\%$

RT = 0.1 nsec, C = 1 pF, rho $\sim -0.025/0.1 \sim -25\%$



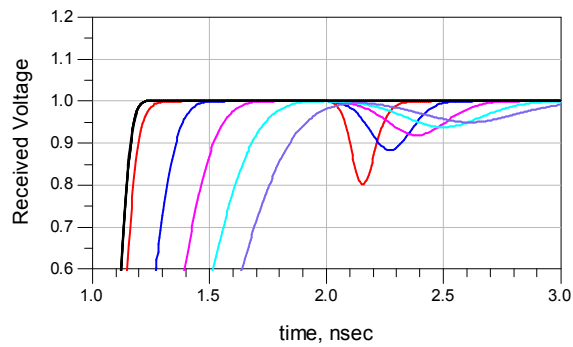
Simulation of Capacitive Discontinuity

RT = 0.1 nsec
 C = 0.2 pF, 0.4 pF, 0.6 pF, 0.8 pF, 1 pF
 Estimate:
 ~ 25% reflected signal from 1 pF cap.
 ~ 10% reflected signal from 0.4 pF



Effect of the Signal Rise Time

C = 1 pF
 RT = 0.1 nsec, 0.2, 0.3, 0.4, 0.5





A Rule of Thumb

$$\rho \sim \frac{-1}{1 + \frac{RT}{25xC}} \sim -\frac{25xC}{RT}$$

For rho < -10%,
 C < 0.1 x RT/25
 C < 0.004 x RT



Keep C < 4 x RT

C in pF, RT in nsec

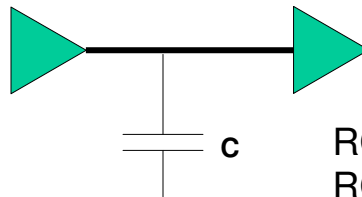
Examples

if RT = 1 nsec, C < 4 pF

If RT = 0.1 nsec, C < 0.4 pF



Delay Adder from Capacitive Loads



RC: $R = \frac{1}{2} Z_0$,
 RC: $R = \frac{1}{2} Z_0 \times C$

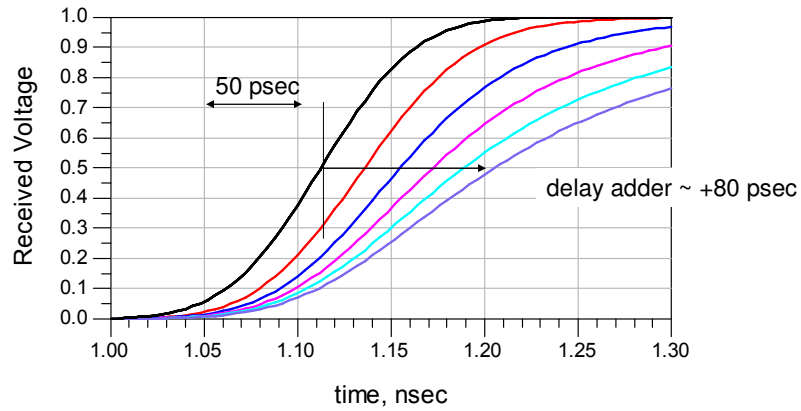
0-50% RT ~ $1.2 \times \frac{1}{2} \times Z_0 \times C \sim 0.03 \times C$ (C in pF, RT in nsec)

Example: 5 pF load: RT ~ $0.03 \times 5 \text{ pF} = 0.150 \text{ nsec}$

$$RT_{output} = \sqrt{RT_{incident}^2 + RT_{cap}^2}$$

$$RT_{output} = \sqrt{0.1^2 + 0.15^2} = 0.18 \text{ nsec}$$

Simulations of Delay Adder



C = 1, 2, 3, 4, 5 pF
RT = 0.1 nsec
Delay adder expected ~ 80 psec

Summary of Design Guidelines For Capacitive Discontinuities

For C in pF and RT in nsec

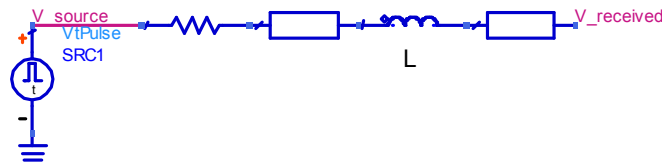
- Reflection noise ~ -2.5% x C/RT
- For < 10% reflected noise, keep C < 4 x RT
- Delay adder is 50%RT ~ 0.03 x C

$$RT_{output} = \sqrt{RT_{incident}^2 + RT_{cap}^2}$$

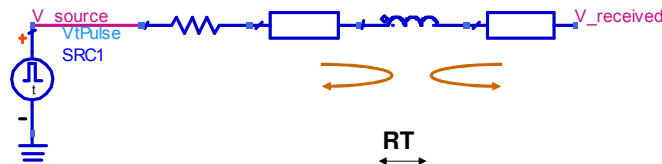


Series L Discontinuities

- Examples
 - ✓ Via transition
 - ✓ Wire bond
 - ✓ Connector lead
 - ✓ Engineering change wire
 - ✓ Gap in the return path



Impact from Series Inductor



$$Z_{\text{inductor}} = \frac{V}{I} = \frac{L \, di}{I \, dt} = \frac{L}{RT}$$

Series impedance

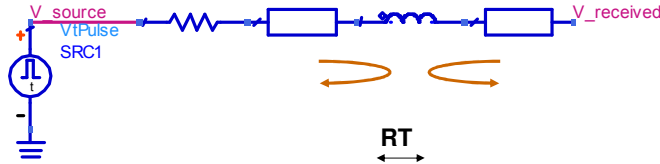
Examples:

$RT = 1 \text{ nsec}, L = 10 \text{ nH}, Z_L \sim 10 \text{ Ohms}$

$RT = 0.1 \text{ nsec}, L = 10 \text{ nH}, Z_L \sim 100 \text{ Ohms}$



Reflections from Series L Discontinuities



$$Z_{\text{inductor}} = \frac{V}{I} = \frac{L \, dI}{I \, dt} = \frac{L}{RT}$$

$$\rho = \frac{(Z_L + Z_0) - Z_0}{(Z_L + Z_0) + Z_0} \sim \frac{Z_L}{2Z_0} \sim \frac{Z_L}{100}$$

Examples:

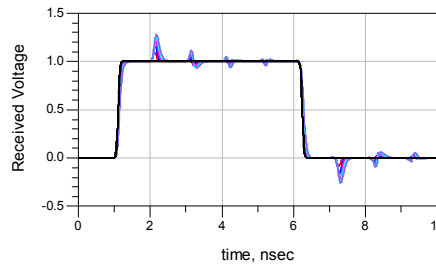
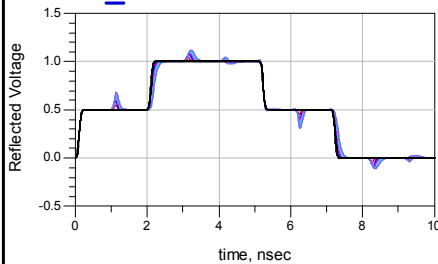
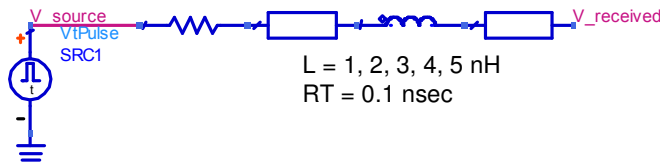
RT = 1 nsec, L = 1 nH, rho ~ 1%

RT = 0.1 nsec, L = 1 nH, rho ~ 10%

$$\rho \sim \frac{L}{100 \times RT}$$

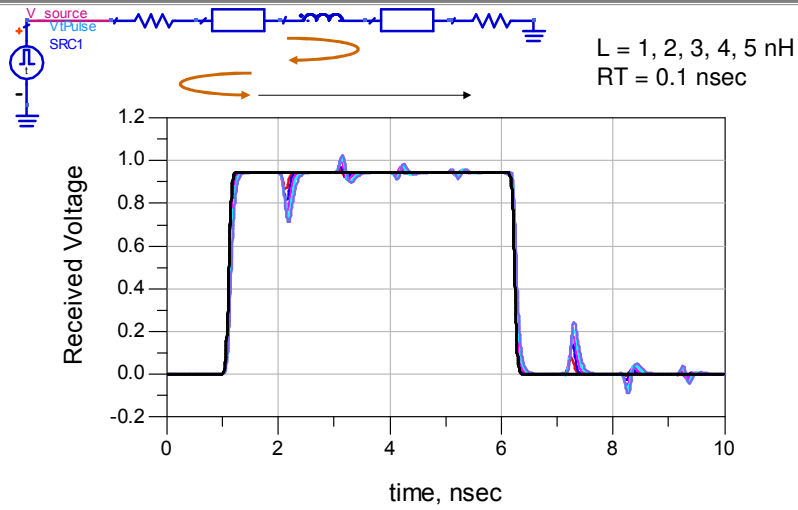


Simulating Inductive Discontinuities: source series terminated



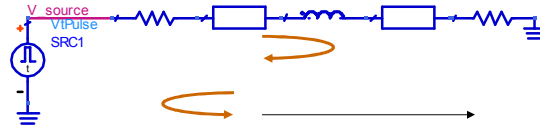


Far End Terminated Circuit



Impact from Reflected Signal

$$\rho \sim \frac{L}{100 \times RT}$$



For noise < 10%,



$$L < 10 \times RT$$

Examples:

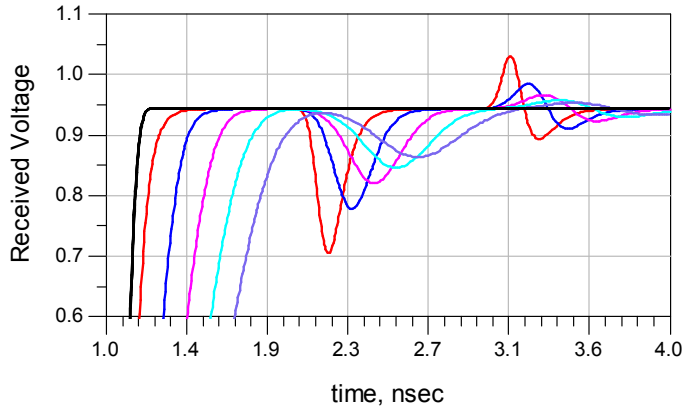
If $RT = 1 \text{ nsec}$, keep $L < 10 \text{ nH}$

If $RT = 0.1 \text{ nsec}$, keep $L < 1 \text{ nH}$

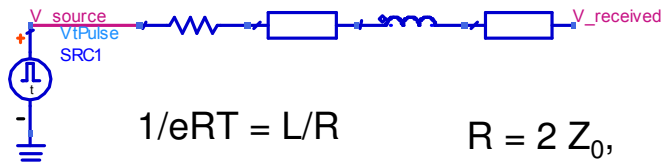


Effect of the Rise Time: far end terminated

L = 5 nH
 Expected rho @ RT = 0.5 nsec = 10%
 RT = 0.1, 0.2, 0.3, 0.4, 0.5 nsec



Delay Adders with Inductive Discontinuities



$$1/eRT = L/R$$

$$R = 2 Z_0,$$

$$1/eRT = L/(2Z_0)$$

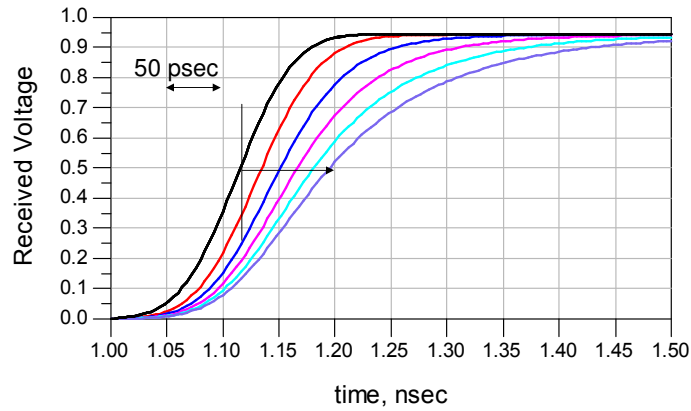
$$0-50\% RT \sim 1.2 \times L/2Z_0 \sim 0.012 \times L \text{ (L in nH, RT in nsec)}$$

Example: 10 nH inductor: 50%RT ~ 0.012 x 10 nH = 0.12 nsec

$$RT_{output} = \sqrt{RT_{incident}^2 + RT_{cap}^2}$$

$$RT_{output} = \sqrt{0.1^2 + 0.12^2} = 0.16nsec$$

Simulating Delay Adders with Inductive Discontinuity

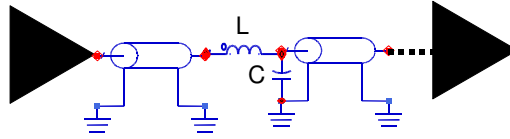


L = 2, 4, 6, 8, 10 nH
RT = 0.1 nsec
Delay adder ~ 60 psec

Summary of Design Guidelines For Inductive Discontinuities

- A series L is a problem for far end terminated lines
- Reflection noise $\sim L/(100 \times RT)$
- For $< 10\%$ reflected noise, keep $L < 10 \times RT$
- The L creates a rise time filter with $50\%RT \sim 0.012 \times L$

Principles of Compensation

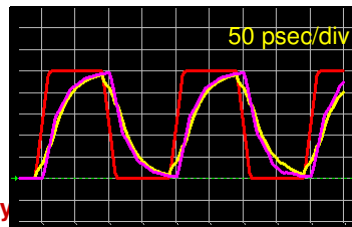
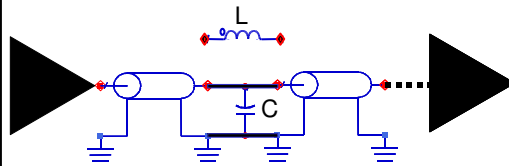


$$\sqrt{\frac{L}{C}} = Z_0$$

$$L = C \times Z_0^2$$

$$C = \frac{L}{Z_0^2}$$

Time Domain Impact From C, L Discontinuities



20 psec RT, No Discontinuity

1 pF C Discontinuity

2.5 nH L Discontinuity

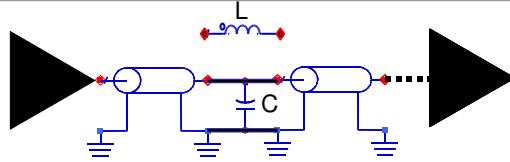
Rise Time Degradation

$$10\%-90\% \text{ RT} = 2.2 \times RC = 2.2 \times \frac{1}{2} \times Z_0 \times C \sim Z_0 \times C = 50 \times 1 \text{ pF} \sim 50 \text{ psec}$$

$$10\%-90\% \text{ RT} = 2.2 \times L/R = 2.2 \times L/(2 \times Z_0) \sim L/Z_0 = 2.5 \text{ nH}/50 \sim 50 \text{ psec}$$



Estimating Compensation L, C



1 pF C Discontinuity

$$L = C \times Z_0^2$$

$$L = 2.5 \text{ nH}$$

2.5 nH L Discontinuity

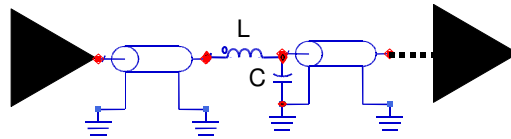
$$C = \frac{L}{Z_0^2}$$

$$C = 1 \text{ pF}$$

$$\sqrt{\frac{2500 \text{ pH}}{1 \text{ pF}}} = 50 \text{ Ohms}$$

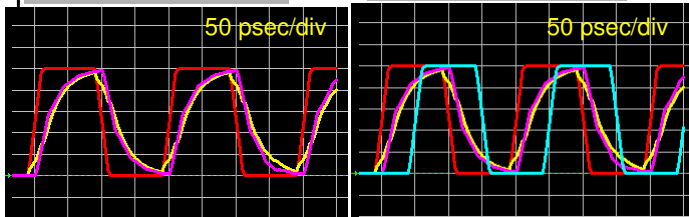


Lumped Discontinuity Compensation: Magic!



C = 1 pF or L = 2.5 nH

C = 1 pF & L = 2.5 nH



$$\sqrt{\frac{2500 \text{ pH}}{1 \text{ pF}}} = 50 \text{ Ohms}$$



Summary of the Design Guideline Rules of Thumb

- For series transmission line discontinuities
 - ✓ Keep the impedance variation within +/- 10 ohms of the line impedance
 - ✓ Keep the length, in inches, shorter than the rise time, in nsec
- For stubs
 - ✓ Try for as high an impedance as possible
 - ✓ Keep the length, in inches, shorter than the rise time, in nsec
- For capacitive discontinuities
 - ✓ Keep the capacitance in pF, less than 4 x RT in nsec
- For inductive discontinuities
 - ✓ Keep the inductance, in nH, less than 10 x RT, in nsec
- For compensation, add extra L or C to keep

$$\sqrt{\frac{L}{C}} = Z_0$$



*Thanks for
listening!*



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课程网址: <http://www.edatop.com/peixun/hfss/122.html>

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详情浏览: <http://www.edatop.com/peixun/antenna/116.html>



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